10/586800

IAP11 Rec'd PCT/PTO 19 JUL 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re	the Application of:	1
Y	DNGXIANG HAN, ET AL.	Art Group:
Applic	Examiner:	
Filed:	July 1 9 , 2006	
For:	Updating Entries Cached by a Network Processor	

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

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Date: July 19, 2006

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Substitute for form 1449A/PTO	Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Application Number Filing Date July 13, 2006 First Named Inventor Yongxiang Han Art Unit	.6	
Sheet of	Examiner Name Attorney Docket Number 42P21623		

		NON PATENT LITERATURE DOCUMENTS
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), wotume-issue number(s), publisher, city and/or country where published.
	1	Intel: Core Router Application; Software Building Blocks Applications Design Guide;
		pages 163-181
	2	Intel: Portability Framework Developer's Manual: Micorengine Programming Models
	2	pages 23-30; Microblocks pages 31-38; Dispatch Loop pages 39-50
	3	Intel: Programmer's Reference Manual: IXP2400 & IXP2800 Network Processor MEv2 Instruction Set; pages 103-232
		instruction set, pages 103-232
	4	Intel: Programmer's Reference Manual; IXP2400 & IXP2800 Network Processor Address
		Maps; pages 233-143; SHaC - Unit Expansion pages 183-198
/JY/	5	Intel Internet Exchange Architecture Portability Framework Developer's Manual; May 200- Document No.: 278662-006;

ı	Signature	75de 10/ (00/51/20	Co	nsidered	08/31/2008	
-	Examiner: Initial if refu	erence considered, whether or no	citation is in conformance with MPEP 609.	Draw [me throu	igh citation if not in conformance and not considered. Inc	dude

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'Applicant's unique citation designation number/Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Sclokoff, Taylor & Zafman (v/r) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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